

# MMBT4401LT1G

## Switching Transistor

### NPN Silicon

#### Features

- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	$V_{CEO}$	40	Vdc
Collector - Base Voltage	$V_{CBO}$	60	Vdc
Emitter - Base Voltage	$V_{EBO}$	6.0	Vdc
Collector Current - Continuous	$I_C$	600	mAdc
Collector Current - Peak	$I_{CM}$	900	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	225 1.8	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	$^\circ\text{C}/\text{W}$
Total Device Dissipation Alumina Substrate (Note 2) @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	300 2.4	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

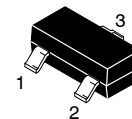
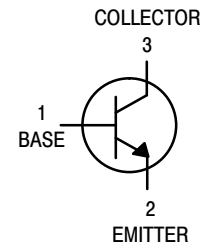
\*Transient pulses must not cause the junction temperature to be exceeded.

1. FR-5 =  $1.0 \times 0.75 \times 0.062$  in.
2. Alumina =  $0.4 \times 0.3 \times 0.024$  in. 99.5% alumina.



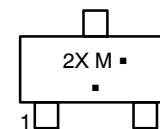
ON Semiconductor®

<http://onsemi.com>



SOT-23 (TO-236)  
CASE 318  
STYLE 6

#### MARKING DIAGRAM



2X = Specific Device Code

M = Date Code\*

■ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

#### ORDERING INFORMATION

Device	Package	Shipping†
MMBT4401LT1G	SOT-23 (Pb-Free)	3000 Tape & Reel
MMBT4401LT3G	SOT-23 (Pb-Free)	10,000 Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MMBT4401LT1G

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Collector - Emitter Breakdown Voltage (Note 3) (I <sub>C</sub> = 1.0 mA <sub>dc</sub> , I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	40	-	V <sub>dc</sub>
Collector - Base Breakdown Voltage (I <sub>C</sub> = 0.1 mA <sub>dc</sub> , I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	60	-	V <sub>dc</sub>
Emitter - Base Breakdown Voltage (I <sub>E</sub> = 0.1 mA <sub>dc</sub> , I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	6.0	-	V <sub>dc</sub>
Base Cutoff Current (V <sub>CE</sub> = 35 V <sub>dc</sub> , V <sub>EB</sub> = 0.4 V <sub>dc</sub> )	I <sub>BEV</sub>	-	0.1	μA <sub>dc</sub>
Collector Cutoff Current (V <sub>CE</sub> = 35 V <sub>dc</sub> , V <sub>EB</sub> = 0.4 V <sub>dc</sub> )	I <sub>CEX</sub>	-	0.1	μA <sub>dc</sub>

## ON CHARACTERISTICS (Note 3)

DC Current Gain (I <sub>C</sub> = 0.1 mA <sub>dc</sub> , V <sub>CE</sub> = 1.0 V <sub>dc</sub> ) (I <sub>C</sub> = 1.0 mA <sub>dc</sub> , V <sub>CE</sub> = 1.0 V <sub>dc</sub> ) (I <sub>C</sub> = 10 mA <sub>dc</sub> , V <sub>CE</sub> = 1.0 V <sub>dc</sub> ) (I <sub>C</sub> = 150 mA <sub>dc</sub> , V <sub>CE</sub> = 1.0 V <sub>dc</sub> ) (I <sub>C</sub> = 500 mA <sub>dc</sub> , V <sub>CE</sub> = 2.0 V <sub>dc</sub> )	h <sub>FE</sub>	20 40 80 100 40	- - - 300 -	-
Collector - Emitter Saturation Voltage (I <sub>C</sub> = 150 mA <sub>dc</sub> , I <sub>B</sub> = 15 mA <sub>dc</sub> ) (I <sub>C</sub> = 500 mA <sub>dc</sub> , I <sub>B</sub> = 50 mA <sub>dc</sub> )	V <sub>CE(sat)</sub>	- -	0.4 0.75	V <sub>dc</sub>
Base - Emitter Saturation Voltage (I <sub>C</sub> = 150 mA <sub>dc</sub> , I <sub>B</sub> = 15 mA <sub>dc</sub> ) (I <sub>C</sub> = 500 mA <sub>dc</sub> , I <sub>B</sub> = 50 mA <sub>dc</sub> )	V <sub>BE(sat)</sub>	0.75 -	0.95 1.2	V <sub>dc</sub>

## SMALL-SIGNAL CHARACTERISTICS

Current - Gain - Bandwidth Product (I <sub>C</sub> = 20 mA <sub>dc</sub> , V <sub>CE</sub> = 10 V <sub>dc</sub> , f = 100 MHz)	f <sub>T</sub>	250	-	MHz
Collector - Base Capacitance (V <sub>CB</sub> = 5.0 V <sub>dc</sub> , I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>cb</sub>	-	6.5	pF
Emitter - Base Capacitance (V <sub>EB</sub> = 0.5 V <sub>dc</sub> , I <sub>C</sub> = 0, f = 1.0 MHz)	C <sub>eb</sub>	-	30	pF
Input Impedance (I <sub>C</sub> = 1.0 mA <sub>dc</sub> , V <sub>CE</sub> = 10 V <sub>dc</sub> , f = 1.0 kHz)	h <sub>ie</sub>	1.0	15	kΩ
Voltage Feedback Ratio (I <sub>C</sub> = 1.0 mA <sub>dc</sub> , V <sub>CE</sub> = 10 V <sub>dc</sub> , f = 1.0 kHz)	h <sub>re</sub>	0.1	8.0	X 10 <sup>-4</sup>
Small - Signal Current Gain (I <sub>C</sub> = 1.0 mA <sub>dc</sub> , V <sub>CE</sub> = 10 V <sub>dc</sub> , f = 1.0 kHz)	h <sub>fe</sub>	40	500	-
Output Admittance (I <sub>C</sub> = 1.0 mA <sub>dc</sub> , V <sub>CE</sub> = 10 V <sub>dc</sub> , f = 1.0 kHz)	h <sub>oe</sub>	1.0	30	μmhos

## SWITCHING CHARACTERISTICS

Delay Time	(V <sub>CC</sub> = 30 V <sub>dc</sub> , V <sub>EB</sub> = 2.0 V <sub>dc</sub> , I <sub>C</sub> = 150 mA <sub>dc</sub> , I <sub>B1</sub> = 15 mA <sub>dc</sub> )	t <sub>d</sub>	-	15	ns
Rise Time		t <sub>r</sub>	-	20	
Storage Time	(V <sub>CC</sub> = 30 V <sub>dc</sub> , I <sub>C</sub> = 150 mA <sub>dc</sub> , I <sub>B1</sub> = I <sub>B2</sub> = 15 mA <sub>dc</sub> )	t <sub>s</sub>	-	225	ns
Fall Time		t <sub>f</sub>	-	30	

3. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.

## SWITCHING TIME EQUIVALENT TEST CIRCUITS

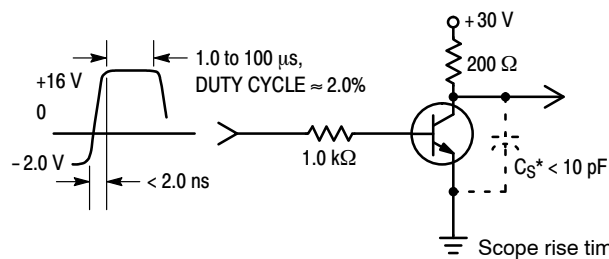


Figure 1. Turn-On Time

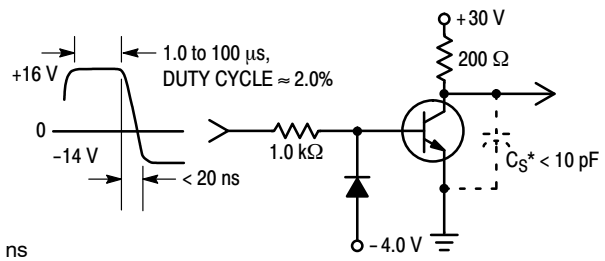


Figure 2. Turn-Off Time

# MMBT4401LT1G

## TRANSIENT CHARACTERISTICS

— 25°C    - - - 100°C

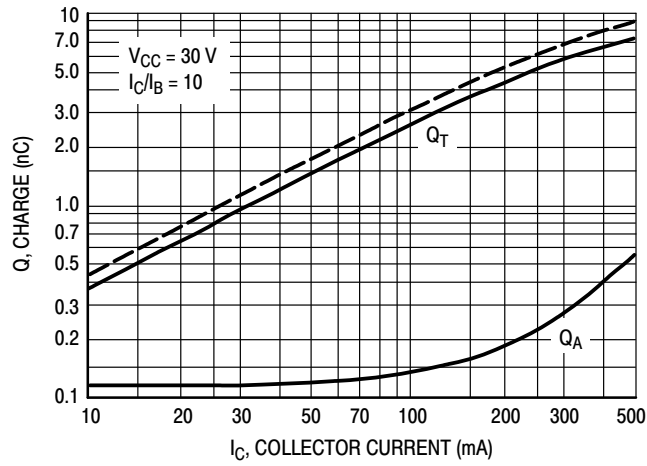


Figure 3. Charge Data

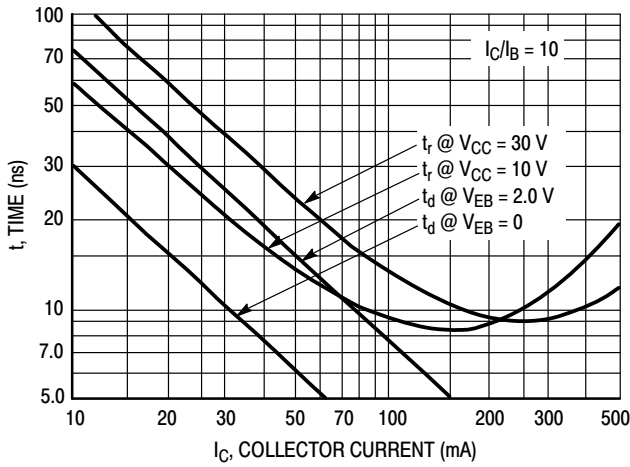


Figure 4. Turn-On Time

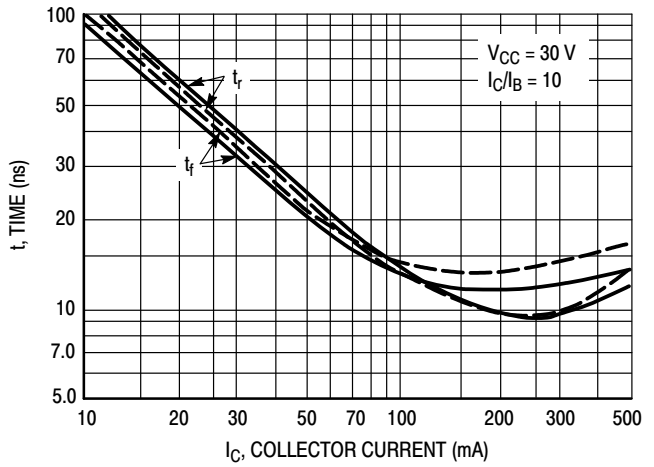


Figure 5. Rise and Fall Times

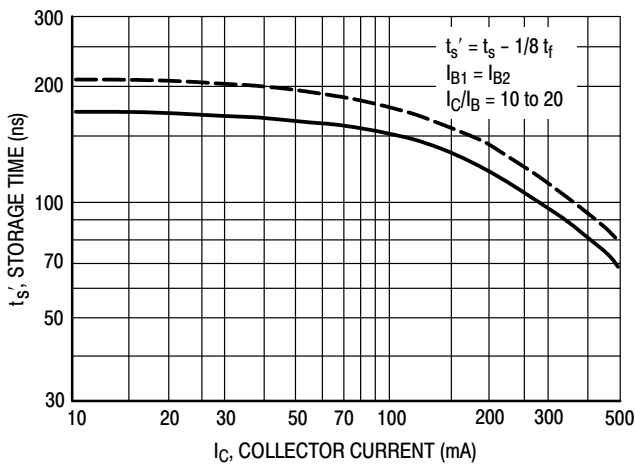


Figure 6. Storage Time

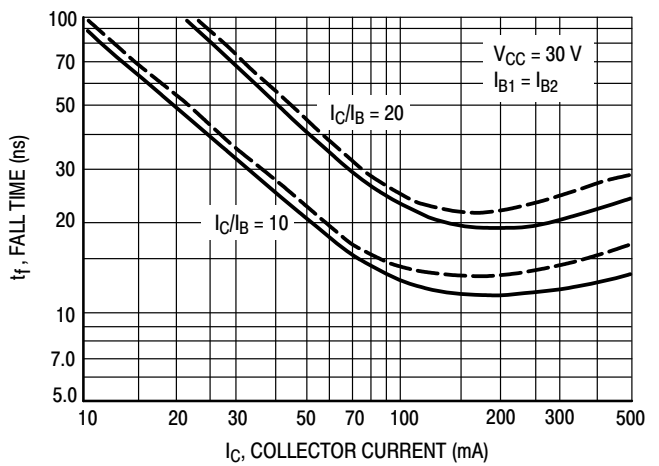


Figure 7. Fall Time

# MMBT4401LT1G

## SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE

$V_{CE} = 10 \text{ Vdc}$ ,  $T_A = 25^\circ\text{C}$ ; Bandwidth = 1.0 Hz

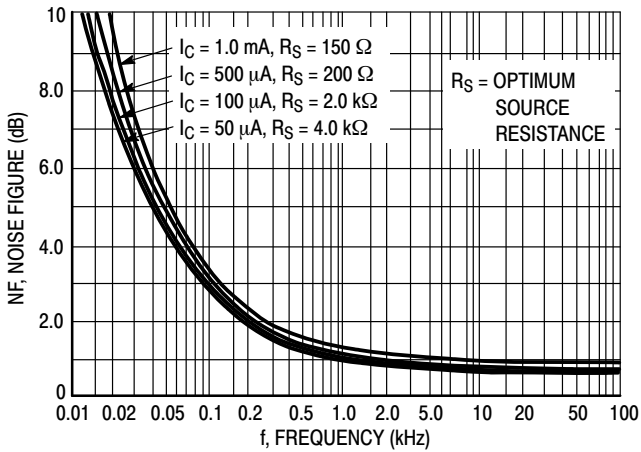


Figure 8. Frequency Effects

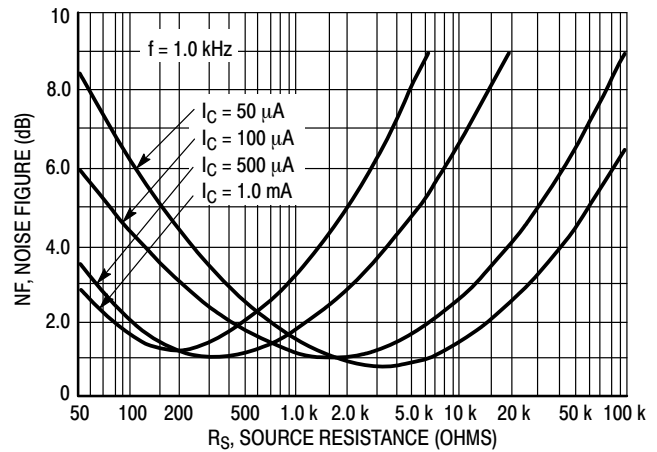


Figure 9. Source Resistance Effects

## h PARAMETERS

$V_{CE} = 10 \text{ Vdc}$ ,  $f = 1.0 \text{ kHz}$ ,  $T_A = 25^\circ\text{C}$

This group of graphs illustrates the relationship between  $h_{fe}$  and other “h” parameters for this series of transistors. To obtain these curves, a high-gain and a low-gain unit were selected from the MMBT4401LT1 lines, and the same units were used to develop the correspondingly numbered curves on each graph.

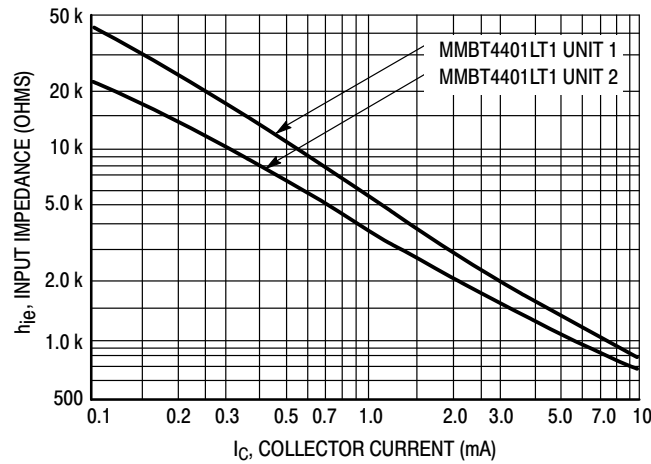


Figure 10. Input Impedance

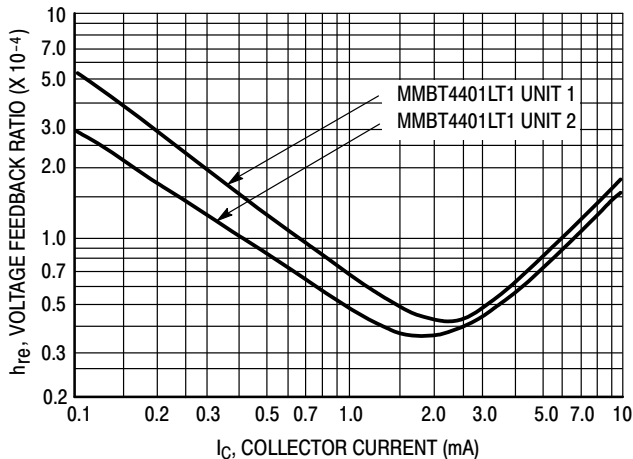


Figure 11. Voltage Feedback Ratio

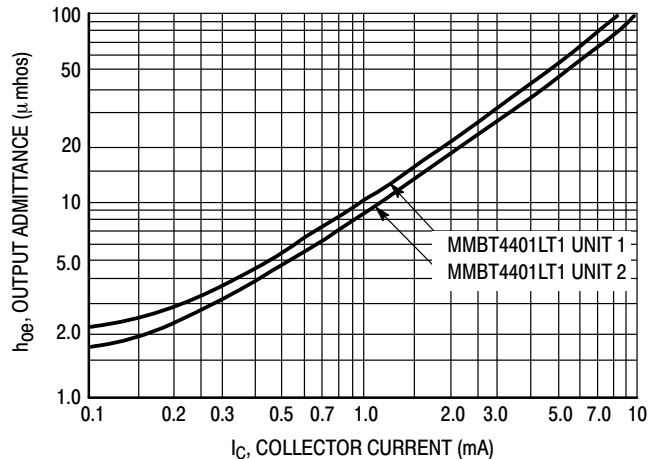


Figure 12. Output Admittance

# MMBT4401LT1G

## STATIC CHARACTERISTICS

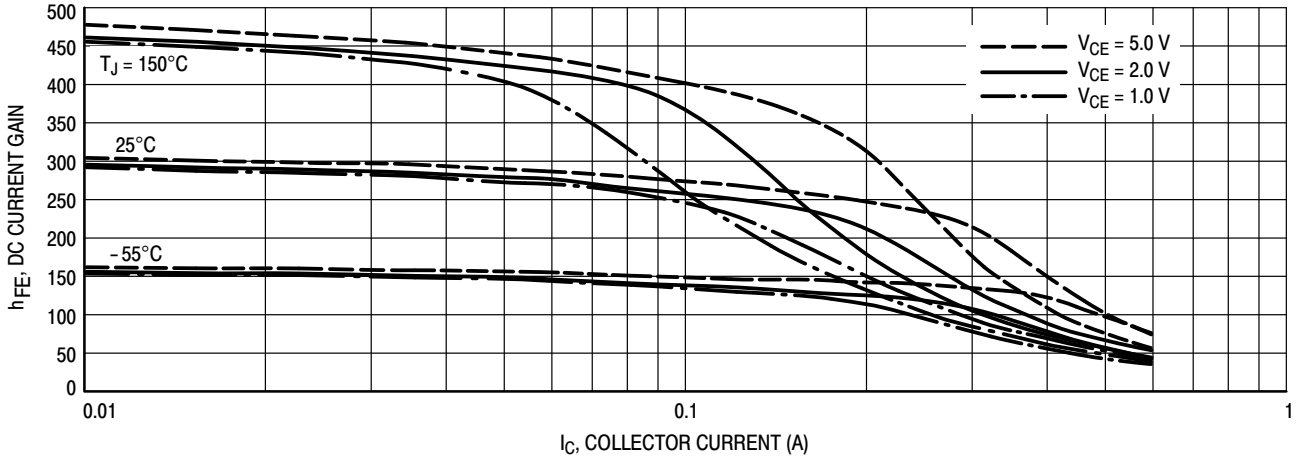


Figure 13. DC Current Gain

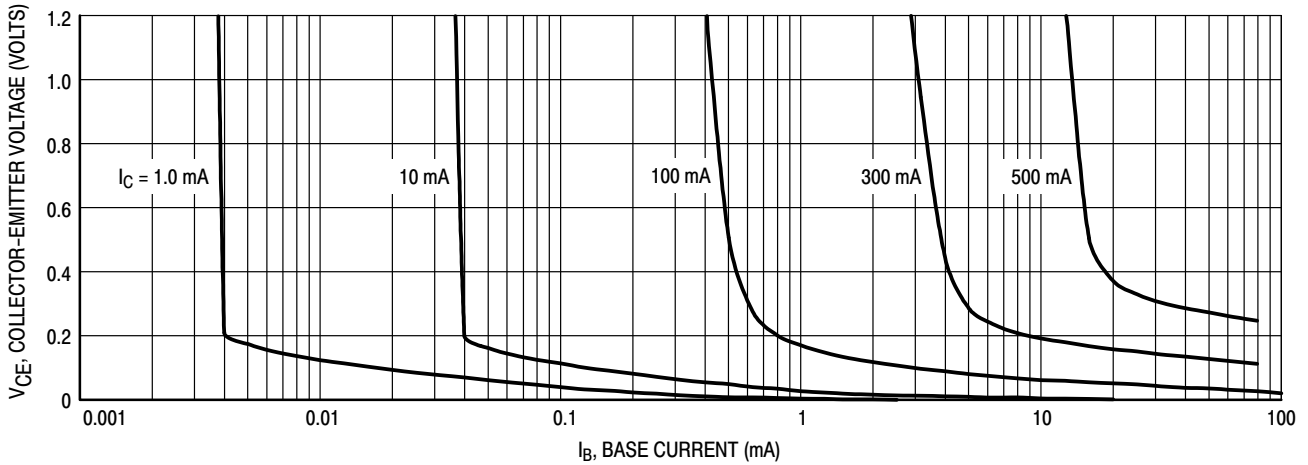


Figure 14. Collector Saturation Region

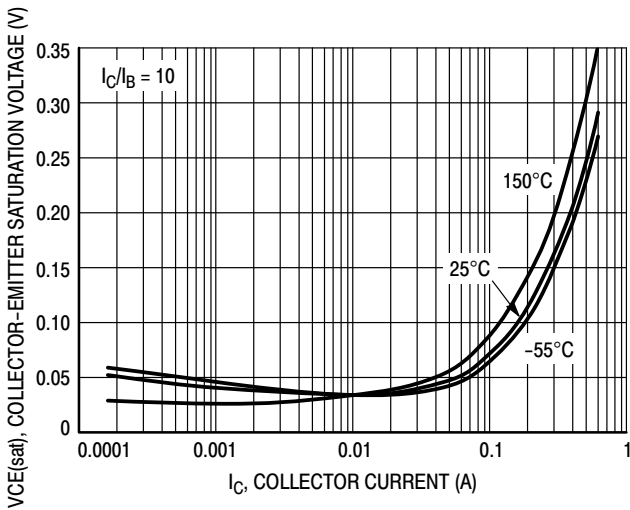


Figure 15. Collector-Emitter Saturation Voltage vs. Collector Current

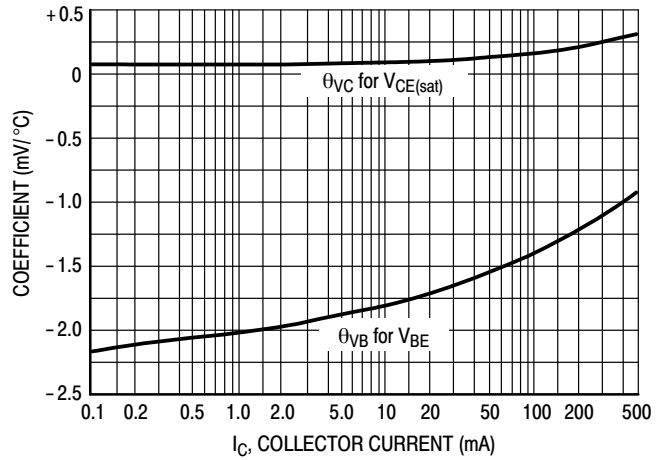
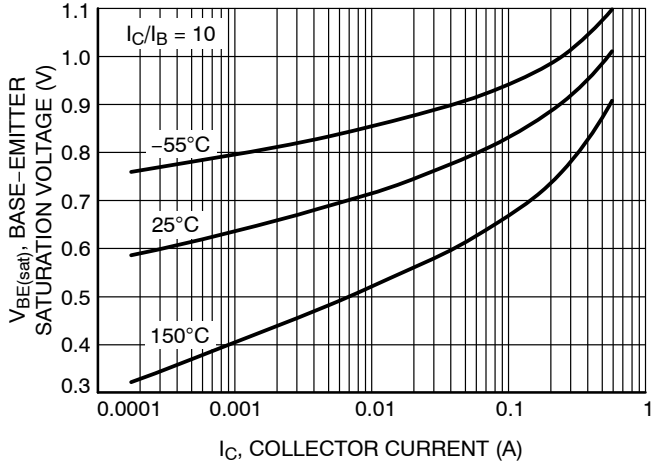


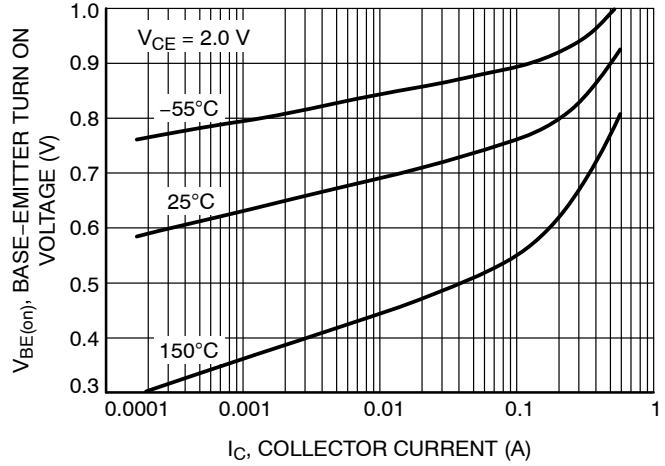
Figure 16. Temperature Coefficients

# MMBT4401LT1G

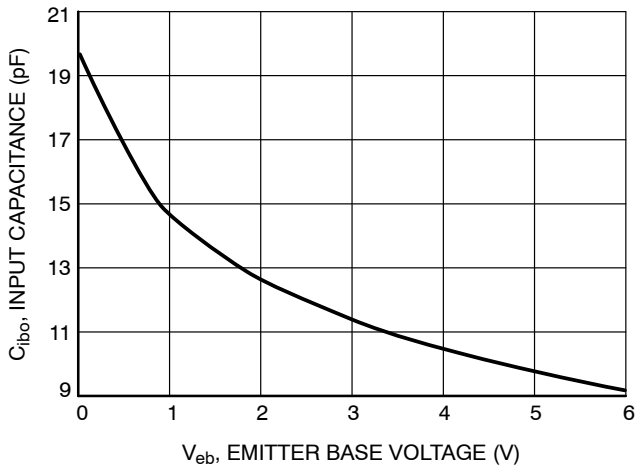
## STATIC CHARACTERISTICS



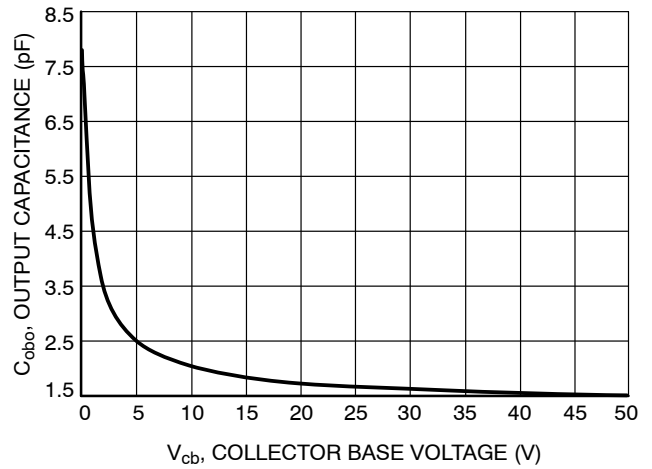
**Figure 17. Base-Emitter Saturation Voltage vs. Collector Current**



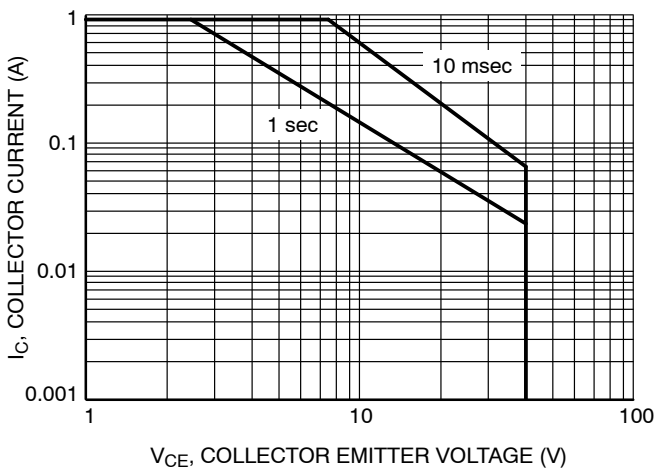
**Figure 18. Base-Emitter Turn On Voltage vs. Collector Current**



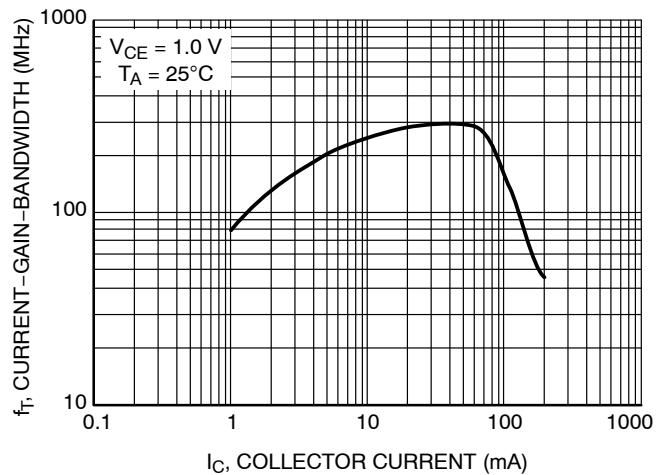
**Figure 19. Input Capacitance vs. Emitter Base Voltage**



**Figure 20. Output Capacitance vs. Collector Base Voltage**



**Figure 21. Safe Operating Area**

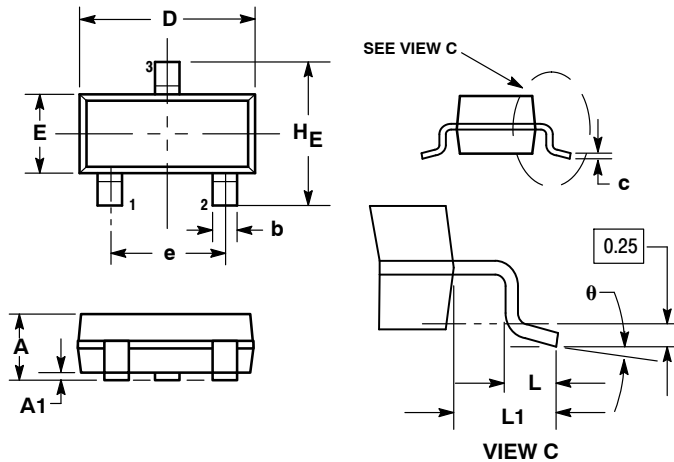


**Figure 22. Current-Gain-Bandwidth Product**

# MMBT4401LT1G

## PACKAGE DIMENSIONS

SOT-23 (TO-236)  
CASE 318-08  
ISSUE AN



NOTES:

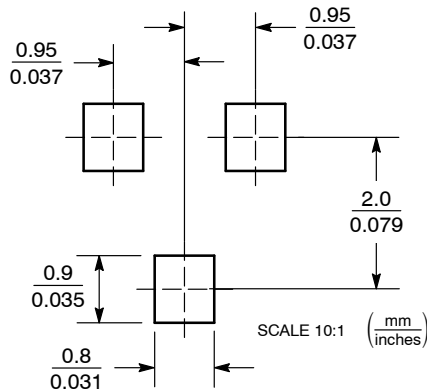
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. 318-01 THRU -07 AND -09 OBSOLETE, NEW STANDARD 318-08.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
c	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
e	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.40	2.64	0.083	0.094	0.104

STYLE 6:

- PIN 1. BASE  
2. EMITTER  
3. COLLECTOR

### SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

### PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:  
Literature Distribution Center for ON Semiconductor  
P.O. Box 5163, Denver, Colorado 80217 USA  
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free  
USA/Canada  
Europe, Middle East and Africa Technical Support:  
Phone: 421 33 790 2910  
Japan Customer Focus Center  
Phone: 81-3-5773-3850

ON Semiconductor Website: [www.onsemi.com](http://www.onsemi.com)  
Order Literature: <http://www.onsemi.com/orderlit>

For additional information, please contact your local Sales Representative